# MODELLING AND SIMULATION OF THREE-PHASE FOUR-SWITCH VOLTAGE SOURCE INVERTER WITH SPACE VECTOR MODULATION 

PHAM CONG DUY<br>Faculty of Electrical Engineering Technology, Industrial University of Ho Chi Minh City<br>dph@iuh.edu.vn<br>DOIs: https://doi.org/10.46242/jstiuh.v60i06.4633


#### Abstract

The three-phase six-switches voltage source inverter is usually used for industrial motor control and renewable energy systems. However, in order to decrease to size and cost of the drive systems, the three-phase four-switch voltage source inverter has been applied to low-cost motor control and renewable energy system. The fundamental concepts and principles of the three-phase four-switch voltage source inverter are discussed. The mathematical model and space vector modulation of the three-phase four-switch voltage source inverter are proposed. The work is demonstrated by simulation results.


Keywords. Three-phase four-switch voltage source inverter, space vector modulation, modelling and simulation, low-cost motor control, low-cost variable frequency drive.

## 1. INTRODUCTION

The key function of a three-phase six-switches voltage source inverter (SSTPI) is to transform a fixed DC voltage to a three-phase AC voltage with variable magnitude and frequency [1] which is consisted of six switches, $S_{1}-S_{6}$, as given in Figure 1a. This inverter type has been used for industrial applications, including motor drives [2], wind power systems [3,4], and solar systems [5,6]. However, in practical applications, reducing inverter size and cost must be considered. To achieve this goal, three-phase inverter with only four switches has also been reported by [7] and it was named three-phase four-switches voltage source inverter (FSTPI). Normally, these power semiconductor switches devices can be IGBT and MOSFET, as seen in Figure 1b. Specially, the midpoint of the dc-link capacitors should be connected to only one phase of motor stator windings [8].
Recently, the FSTPIs have been proposed for low-cost brushless DC motor drive applications in [9-11]. In [9], a current control scheme of brushless DC motors driven by four-switch three-phase inverters was proposed. The direct torque control method of brushless DC motors to reduce torque ripple during sector-to-sector commutations has been shown in [10]. There has been a significant research interest to control of induction motors [12-15]. In [12], a performance investigation of FSTPI-fed induction motor drives at low speeds using fuzzy logic and PI controllers has been shown. The direct torque control method [13], the predictive torque control [14], and the finite-control-set model predictive control [15] for FSTPI-fed induction motor drives have been studied.
This inverter has been utilized in control to PMSM drives [16-18]. The influence and compensation of inverter voltage drop in direct torque control [16], predictive current control [17], and high-frequency voltage injection sensorless control for the FSTPI-fed permanent magnet synchronous motor drives. The other approach for this inverter was reported in [19] with synchronous reluctance motors drive systems.
This inverter has been intensively investigated to wind power [20-23]. In [20,21], the direct torque and power control techniques for fault-tolerant back-to-back converter for direct-drive PMSG wind turbines was considered. The two FSTPIs are connected to get a low-cost back-to-back converter for PMSG wind turbines using the hysteresis current controllers which was proposed in [22]. Application of the FSTPIs to connect renewable energy source to a generalized unbalanced microgrid system was shown in [23].
Correspondingly, various pulse width modulation (PWM) techniques for FSTPIs have been proposed in the literature [1]. With the research and development of digital signal processors, microcontrollers, and microprocessors, the space-vector modulation, a technique that get to less harmonic distortion of the load current and low switching losses that will not be required to use the add hardware devices. This technique uses a concept of a space vector, and it was named space vector pulse width modulation (SVPWM) [1]. For developing low-cost energy-saving products, the mathematical model and space vector modulation of the three-phase four-switch voltage source inverter technology are proposed in the paper. The paper will be
focused on both the undergraduate and postgraduate students of electrical engineering and automation control engineering at the Industrial University of Ho Chi Minh City, Vietnam.


Figure 1: Circuit diagrams of the inverter-fed three-phase load. (a) SSTPI and (b) FSTPI.
Figure 1 shows the circuit diagrams of the inverter-fed three-phase load. Figure 1a shows the circuit diagram of the SSTPI and Figure 1b shows the circuit diagram of the FSTPI.
In this paper, the mathematical model, SVM, and the flowchart of the SVM algorithm implementation of three-phase four-switch inverter are proposed.
The main parts of the paper are briefly given as follows: Section 1 gives an introduction of SSTPIs and FSTPIs. Then, Section 2 recalls the model of the FSTPI. Section 3 proposes the flowchart of the SVM algorithm implementation. Numerical results are given in Section 4. Section 5 summarizes the conclusion of the paper.

## 2. Model of Three-phase four-switch voltage source inverter

In this section, the model of FSTPI is shown in Fig. 1b.
For simplicity, the switching states for each phase are the switching states (either off or on) which can be obtained as follows:

$$
\left\{\begin{array}{l}
S_{b}=S_{3}  \tag{1}\\
S_{c}=S_{5}
\end{array}\right.
$$

The pole voltages of FSTPI can be calculated based on the switching states and the dc-link two-capacitor voltages $U_{\mathrm{dc} 1}$ and $U_{\mathrm{dc} 2}$

$$
\left\{\begin{array}{l}
u_{a o}=0  \tag{2}\\
u_{b o}=S_{b} U_{\mathrm{dcl}}+\left(S_{b}-1\right) U_{\mathrm{dc} 2} \\
u_{c o}=S_{c} U_{\mathrm{dcl} 1}+\left(S_{c}-1\right) U_{\mathrm{dc} 2}
\end{array}\right.
$$

Under the assumption of three-phase balanced operation conditions, the phase-to-neutral voltages are listed as follows [16]

$$
\left\{\begin{array}{l}
u_{a n}=\frac{2}{3} u_{a o}-\frac{1}{3}\left(u_{b o}+u_{c o}\right)  \tag{3}\\
u_{b n}=\frac{2}{3} u_{b o}-\frac{1}{3}\left(u_{c o}+u_{a o}\right) \\
u_{c n}=\frac{2}{3} u_{c o}-\frac{1}{3}\left(u_{a o}+u_{b o}\right)
\end{array}\right.
$$

From Figure 1b, with combination of the $S_{b}$ and $S_{c}$ switches, there are four switching status of $S_{b}$ and $S_{c}$ switches, such as, (off, off), (off, on), (on, off), and (on, on) which are shown in Figure 2a, Figure 2b, Figure 2c, and Figure 2d, respectively.

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Applying to Kirchhoff's voltage law of Figure 2, the sector number, switching states, and the output voltages of the FSTPI are listed in Table 1.


Figure 2: The four switching states. (a) $S_{b}=\mathrm{off}, S_{c}=\mathrm{off}$, (b) $S_{b}=\mathrm{off}, S_{c}=\mathrm{on}$, (c) $S_{b}=\mathrm{on}, S_{c}=$ off , (d)

$$
S_{b}=\mathrm{on}, S_{c}=\mathrm{on}
$$

Table 1: Switching state and the output voltages of the FSTPI

| Sector <br> number | Sector <br> volatge | Switching state <br> $\left(S_{b}, S_{c}\right)$ | Ouput volatge <br> $u_{a n}$ | Ouput volatge <br> $u_{b n}$ | Ouput volatge <br> $u_{c n}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | $U_{1}$ | off, off | $\frac{2}{3} U_{\mathrm{dc} 2}$ | $-\frac{1}{3} U_{\mathrm{dc} 2}$ | $-\frac{1}{3} U_{\mathrm{dc} 2}$ |
| 2 | $U_{2}$ | off, on | $\frac{1}{3}\left(U_{\mathrm{dc} 2}-U_{\mathrm{dc} 1}\right)$ | $-\frac{1}{3}\left(2 U_{\mathrm{dc} 2}+U_{\mathrm{dcl}}\right)$ | $\frac{1}{3}\left(2 U_{\mathrm{dcl} 1}+U_{\mathrm{dc} 2}\right)$ |
| 3 | $U_{3}$ | on, off | $\frac{1}{3}\left(U_{\mathrm{dc} 2}-U_{\mathrm{dc} 1}\right)$ | $\frac{1}{3}\left(2 U_{\mathrm{dc1} 1}+U_{\mathrm{dc} 2}\right)$ | $-\frac{1}{3}\left(2 U_{\mathrm{dc} 2}+U_{\mathrm{dcl} 1}\right)$ |
| 4 | $U_{4}$ | on, on | $-\frac{2}{3} U_{\mathrm{dc} 1}$ | $\frac{1}{3} U_{\mathrm{dcl}}$ | $\frac{1}{3} U_{\mathrm{dc} 1}$ |

Based on Table 1, the phase-to-neutral voltages are functions of the $S_{b}$ and $S_{c}$ switches and the dc-link two-capacitor voltages $U_{\mathrm{dc1}}$ and $U_{\mathrm{dc} 2}$, then there is

$$
\left\{\begin{array}{l}
u_{a n}=\frac{U_{\mathrm{dc} 1}}{3}\left(-S_{b}-S_{c}\right)+\frac{U_{\mathrm{dc} 2}}{3}\left(2-S_{b}-S_{c}\right)  \tag{4}\\
u_{b n}=\frac{U_{\mathrm{dc} 1}}{3}\left(2 S_{b}-S_{c}\right)+\frac{U_{\mathrm{dc} 2}}{3}\left(2 S_{b}-S_{c}-1\right) \\
u_{c n}=\frac{U_{\mathrm{dc} 1}}{3}\left(2 S_{\mathrm{c}}-S_{\mathrm{b}}\right)+\frac{U_{\mathrm{dc} 2}}{3}\left(2 S_{\mathrm{c}}-S_{\mathrm{b}}-1\right)
\end{array}\right.
$$

Considering the dc-link voltage value of upper capacitance and the dc-link voltage value of lower capacitance are equal, given by

$$
\begin{equation*}
U_{\mathrm{dcl}}=U_{\mathrm{dc} 2}=\frac{U_{\mathrm{dc}}}{2} \tag{5}
\end{equation*}
$$

Substituting (5) into (4), we have

$$
\left\{\begin{array}{l}
u_{a n}=\frac{U_{\mathrm{dc}}}{6}\left(2-2 S_{b}-2 S_{c}\right)  \tag{6}\\
u_{b n}=\frac{U_{\mathrm{dc}}}{6}\left(4 S_{b}-2 S_{c}-1\right) \\
u_{c n}=\frac{U_{\mathrm{dc}}}{6}\left(4 S_{\mathrm{c}}-2 S_{\mathrm{b}}-1\right)
\end{array}\right.
$$

Equation (6) shows the mathematical model of the three-phase four-switch voltage source inverter.

## 3. Space Vector Modulation

This section presents the theory and implementation of the space vector modulation for the FSTPI. The main task of the SVPWM is to determine the switching signals for the FSTPI, then generating the threephase voltages with variable magnitude and frequency to the three-phase loads. By combining the switching states and the space vectors, the reference voltage vector $v_{\text {ref }}$ was created instantaneously.
The three-phase control voltages are given by $[24,25]$

$$
\left\{\begin{array}{l}
v_{\mathrm{as}}^{*}=\sqrt{2} V \cos (2 \pi f t)  \tag{7}\\
v_{\mathrm{bs}}^{*}=\sqrt{2} V \cos \left(2 \pi f t-\frac{2 \pi}{3}\right) \\
v_{\mathrm{cs}}^{*}=\sqrt{2} V \cos \left(2 \pi f t-\frac{4 \pi}{3}\right)
\end{array}\right.
$$

in which $V$ is the rms value of the control phase voltages and $f$ is the frequency of the control phase voltages
To implement the SVPWM, three-phase abc reference voltages are converted into the alpha-beta reference voltages using the Clark transformation. Therefore, there is

$$
\left\{\begin{array}{l}
v_{\mathrm{as}}^{*}=v_{\mathrm{as}}^{*}  \tag{8}\\
v_{\mathrm{Bs}}^{*}=\frac{2}{\sqrt{3}}\left(v_{\mathrm{bs}}^{*}-v_{\mathrm{cs}}^{*}\right)
\end{array}\right.
$$

Next, the magnitude and angular of the reference voltage vector are calculated from the alpha-beta reference voltages which may be rewritten as

$$
\left\{\begin{array}{l}
v_{\mathrm{sef}}^{*}=U_{s}=\sqrt{\left(v_{\mathrm{as}}^{*}\right)^{2}+\left(v_{\mathrm{Bs}}^{*}\right)^{2}}  \tag{9}\\
\theta=\tan ^{-1} \frac{v_{\beta s}^{*}}{v_{\mathrm{as}}^{*}}
\end{array}\right.
$$

The modified angular $\theta_{n}$ are calculated from the $\theta$ angular, then there is

$$
\begin{equation*}
\theta_{n}=\frac{180}{\pi} \theta \tag{10}
\end{equation*}
$$

The sector number are calculated from the modified angular $\theta_{n}$

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$$
\mathrm{n}=\left\{\begin{array}{l}
1,\left(\theta_{n}>0^{\circ} \text { and } \theta_{n} \leq 90^{\circ}\right)  \tag{11}\\
2,\left(\theta_{n}>90^{\circ} \text { and } \theta_{n} \leq 180^{\circ}\right) \\
3,\left(\theta_{n} \geq-180^{\circ} \text { and } \theta_{n} \leq-90^{\circ}\right) \\
4,\left(\theta_{n} \geq-90^{\circ} \text { and } \theta_{n} \leq 0^{\circ}\right)
\end{array}\right.
$$

The modulation index can be calculated by

$$
\begin{equation*}
m_{\mathrm{a}}=\frac{\sqrt{3} U_{\mathrm{s}}}{U_{\mathrm{dc}}} \tag{12}
\end{equation*}
$$

The reference voltage vector is synthesized using vectors $U_{1}$ to $U_{4}$

$$
\begin{equation*}
\int_{0}^{T_{s}} U_{s} d t=\int_{0}^{T_{00}} U_{1} d t+\int_{T_{00}}^{T_{00}+T_{10}} U_{2} d t+\int_{T_{00}+T_{10}}^{T_{00}+T_{10}+T_{11}} U_{3} d t+\int_{T_{00}+T_{10}+T_{11}}^{T_{00}+T_{10}+T_{11}+T_{01}} U_{4} d t \tag{13}
\end{equation*}
$$

The dwell times for the vectors $U_{1}$ to $U_{4}$

$$
\left\{\begin{array}{l}
U_{s} T_{\mathrm{s}}=U_{1} T_{00}+U_{2} T_{10}+U_{3} T_{11}+U_{4} T_{01}  \tag{14}\\
T_{\mathrm{s}}=T_{00}+T_{10}+T_{11}+T_{01}
\end{array}\right.
$$

The duty ratios can be calculated by

$$
\left\{\begin{array}{l}
T_{\mathrm{b}}=\frac{T_{\mathrm{s}}}{2}\left(-T_{00}+T_{10}+T_{11}-T_{01}\right)  \tag{15}\\
T_{\mathrm{c}}=\frac{T_{\mathrm{s}}}{2}\left(-T_{00}-T_{10}+T_{11}+T_{01}\right)
\end{array}\right.
$$

Finally, the duty ratios $T_{\mathrm{b}}$ and $T_{\mathrm{c}}$ are comparing with the carrier wave tri to the switches $S_{b}$ and $S_{c}$ [24,25].

$$
\left\{\begin{array}{l}
S_{b}=T_{\mathrm{b}} \geq t r i  \tag{16}\\
S_{c}=T_{\mathrm{c}} \geq t r i
\end{array}\right.
$$

Table 2: The sector and the dwell times of the FSTPI

| Sector I | Sector II |
| :--- | :--- |
| $T_{00}=\frac{T_{\mathrm{s}}}{2}-m_{\mathrm{a}} T_{\mathrm{s}} \sin \left(\theta-\frac{\pi}{3}\right)$ | $T_{00}=0$ |
| $T_{01}=0$ | $T_{01}=\frac{T_{\mathrm{s}}}{2}-m_{\mathrm{a}} T_{\mathrm{s}} \sin \left(\theta-\frac{\pi}{3}\right)$ |
| $T_{10}=m_{\mathrm{a}} T_{\mathrm{s}} \sin (\theta)$ | $T_{10}=\frac{T_{\mathrm{s}}}{2}-m_{\mathrm{a}} T_{\mathrm{s}} \sin \left(\theta+\frac{\pi}{3}\right)$ |
| $T_{11}=\frac{T_{\mathrm{s}}}{2}-m_{\mathrm{a}} T_{\mathrm{s}} \sin \left(\theta+\frac{\pi}{3}\right)$ | $T_{11}=-\sqrt{3} m_{\mathrm{a}} T_{\mathrm{s}} \sin (\theta)$ |
| $\operatorname{Sector~III}$ | $\operatorname{Sector~IV}$ |
| $T_{00}=\frac{T_{\mathrm{s}}}{2}+m_{\mathrm{a}} T_{\mathrm{s}} \sin \left(\theta+\frac{\pi}{3}\right)$ | $T_{00}=\sqrt{3} m_{\mathrm{a}} T_{\mathrm{s}} \sin (\theta)$ |
| $T_{01}=-m_{\mathrm{a}} T_{\mathrm{s}} \sin (\theta)$ | $T_{01}=\frac{T_{\mathrm{s}}}{2}-m_{\mathrm{a}} T_{\mathrm{s}} \sin \left(\theta+\frac{\pi}{3}\right)$ |
| $T_{10}=0$ | $T_{10}=\frac{T_{\mathrm{s}}}{2}+m_{\mathrm{a}} T_{\mathrm{s}} \sin \left(\theta-\frac{\pi}{3}\right)$ |
| $T_{11}=\frac{T_{\mathrm{s}}}{2}+m_{\mathrm{a}} T_{\mathrm{s}} \sin \left(\theta-\frac{\pi}{3}\right)$ | $T_{11}=0$ |

Based on (14), we have Table 2. There are six main steps for the implementation process of the SVM.

First, according to (7), the control phase voltages $v_{\mathrm{as}}^{*}, v_{\mathrm{bs}}^{*}$, and $v_{\mathrm{cs}}^{*}$ are the initial conditions of the step 1 .


Figure 3: Flowchart of the SVM algorithm implementation
Then, the step 2 uses the Clark transformation to find the control phase voltages $v_{a s}^{*}$ and $v_{\beta s}^{*}$. The step 3 is implemented to find the $\nu_{\mathrm{ref}}^{*}$ and $\theta^{\prime}$. Finally, the switching signals for the FSTPI are generated by comparing duty ratios $T_{\mathrm{b}}$ and $T_{\mathrm{c}}$ to the carrier wave tri. Figure 3 shows the flowchart of the implementation of space vector modulation of the three-phase four-switch voltage source inverter.

## 4. Simulation Results And Discussion

By demonstrating the performance of SVM mentioned above, a program is developed in Matlab/Simulink environment. The parameters of the simulation system are listed in Table 3 and Table 4. The dc-link voltage is 550 V , amplitude value of phase voltages is 220 V , control frequency is 50 Hz , switching frequency is 4 kHz.
The simulated waveforms for the SVM are shown in Figure 4. Figure 4a shows how the phase voltages of the control waveform is obtained which are equal to those of the three-phase reference voltage having the rms value of phase voltages is 220 V and the frequency is 50 Hz . Figure 4 b shows the alpha-beta reference voltages and they also have the rms value of phase voltages is 220 V and the frequency is 50 Hz . Figure 4c shows the magnitude of the reference voltage which is 230 V . Figure 4 d shows the angular of the reference

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voltage. Figure 4 e shows sector number from 4 to 1 . These results are the same as Equation 11 . It can be observed from Figure 4 f that the modulation index is 0.73 . Figure 4 g shows the duty ratios for the switches $S_{b}$ and $S_{c}$. Figure 4h shows the gate signal of the switches $S_{b}$ and $S_{c}$. It can be shown that the gate signal of the switch $S_{b}$ and the gate signal of the switch $S_{c}$ are opposite each other.
Figure 5 shows the performance of three-phase load including the load voltages and the load currents. Figure 5a shows the load voltage of the $a$-phase. Figure 5 b shows the load voltage of the $b$-phase. Figure 5c shows the load voltage of the $c$-phase. Figure 5d shows the three-phase load currents which is close to sinusoidal waveform and the magnitude value of them are about 2 A .

(a)

(b)

(c)

(e)

(f)

(g)

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(h)

Figure 4: SVM inverter. (a) phase voltages of the control, (b) the alpha-beta reference voltages, (c) the magnitude of the reference voltage, (d) the angular of the reference voltage, (e) sector number, (f) the modulation index, (g) the duty ratios, (h) the gate signals.

(a)

(b)



Figure 5: Performance of three-phase load. (a) the load voltage of the $a$-phase, (b) the load voltage of the $b$-phase, (c) the load voltage of the $c$-phase, (d) the three-phase load currents.

Table 3: IGBT parameters

| Symbol | Value | Descriptions |
| :---: | :---: | :---: |
| $R_{\text {on }}$ | $0.001 \Omega$ | Internal resistance |
| $R_{\mathrm{s}}$ | $100 \mathrm{k} \Omega$ | Snubber resistance |
| $C_{\mathrm{s}}$ | $\inf$ | Snubber capacitive |

Table 4: Load parameters

| Symbol | Value | Descriptions |
| :---: | :---: | :---: |
| $R$ | $60 \Omega$ | Load resistance |
| $L$ | 0.06 H | Load inductance |

## 5. CONCLUSIONS

This paper has presented the SVPWM for FSTP. The simulation results demonstrate the proposed approach. The approach can easily be implemented in low-cost reduced switch inverters. This paper is also a reference for the students in the field of electrical machines control. In addition, this approach can also be extended for pump and fan solutions in high-tech agriculture applications.
Future works will been consider the processor-in-the-loop approach such as LAUNCHXL-F28069M for FSTPs to get reduction of the total harmonic distortion and common mode voltage in low-cost motor control and renewable energy systems.

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# MÔ HÌNH VÀ MÔ PHỎNG BIẾN TẦN BA PHA BỐN KHÓA VỚI QUÁ ĐIỀU CHẾ VECTOR KHÔNG GIAN 

PHAM CÔNG DUY<br>Khoa Công nghệ Điện, Trường Đại học Công nghiệp Thành phố Hồ Chí Minh. dph@iuh.edu.vn

Tóm tắt. Biến tần nguồn áp ba pha sáu khóa được sử dụng cho điều khiển động cơ công nghiệp và hệ thống năng lượng tái tạo. Tuy nhiên, để giảm kích thước và giá thành của hệ thống truyền động, biến tần nguồn áp ba pha bốn khóa đã được áp dụng để điều khiển động cơ công suất thấp và hệ thống năng lượng tái tạo công suất thấp. Các khái niệm và nguyên lý cơ bản của bộ biến tần ba pha bốn khóa được thảo luận. Mô hình toán học và phương pháp điều chế vector không gian áp dụng cho bộ biến tần này được đề xuất. Công việc được chứng minh bởi kết quả mô phỏng.
Từ khóa. Biến tần nguồn áp bốn khóa ba pha, quá điều chế vector không gian, mô hình và mô phỏng, điều khiển động cơ giá thấp, truyền động tần số thay đổi giá thấp.

